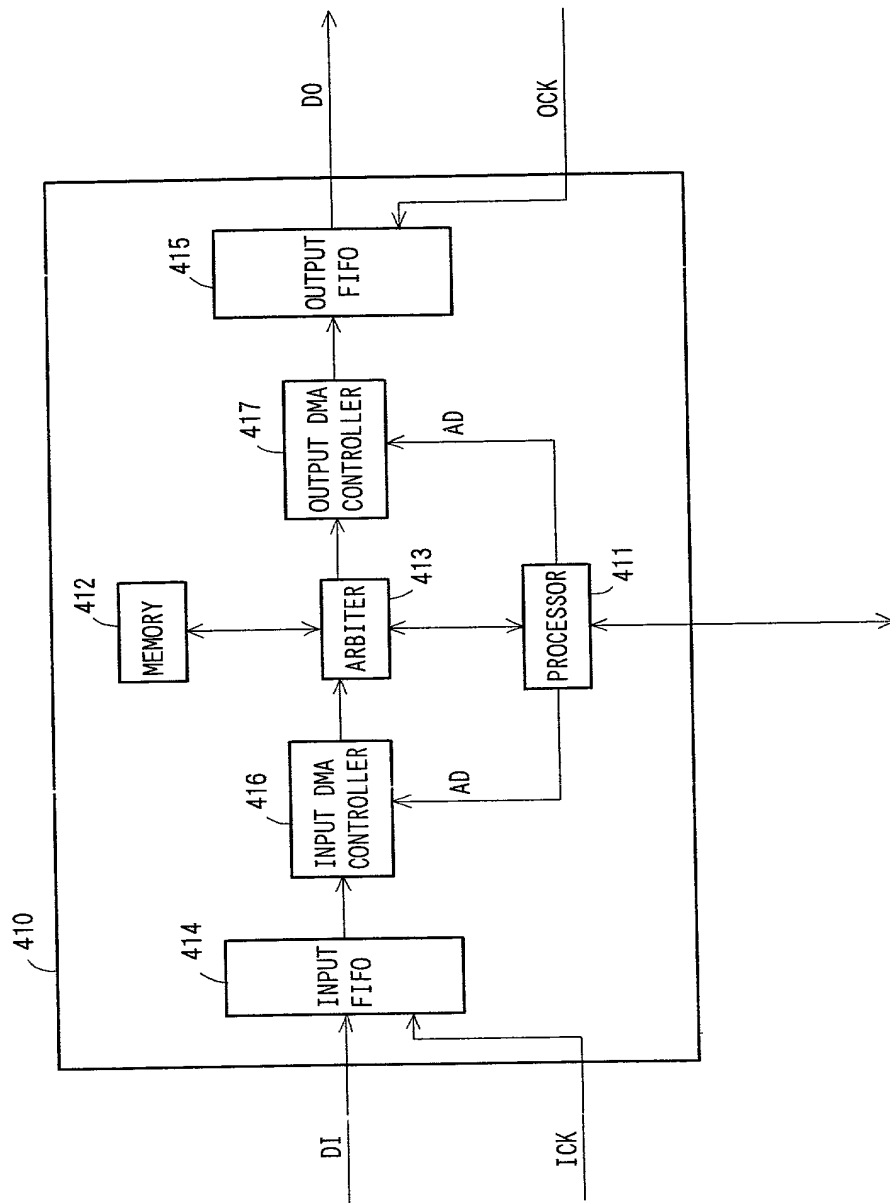
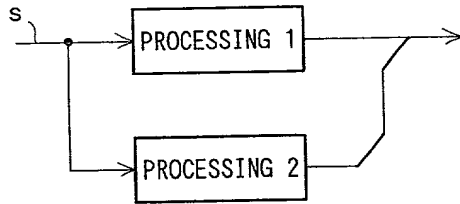


FIG. 1

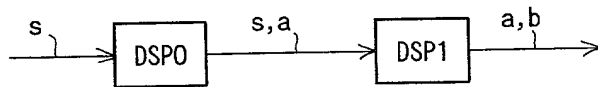
FIG. 2



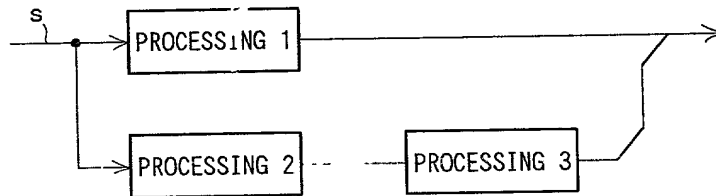
**FIG. 3A**



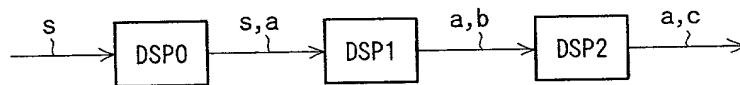
**FIG. 3B**



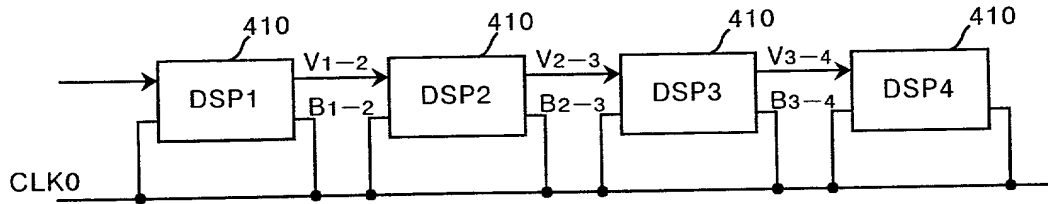
**FIG. 4A**



**FIG. 4B**



# FIG.5A



# FIG.5B

	DSP1 → DSP2	DSP2 → DSP3	DSP3 → DSP4
V	70 ⇒ 90	110 ⇒ 132	120 ⇒ 128
B	4	6	8
V/B	17.5 ⇒ <u>22.5</u>	<u>18.3</u> ⇒ 22	15.0 ⇒ 16.0
			⇒CHANGED DATA VOLUME

# FIG.5C

	DSP1 → DSP2	DSP2 → DSP3	DSP3 → DSP4
V	70 ⇒ 90	110 ⇒ <u>132</u>	<u>120</u> ⇒ 128
B	8	8	8
V/B	8.75 ⇒ 11.25	13.75 ⇒ <u>16.5</u>	<u>15.0</u> ⇒ 16.0
			⇒CHANGED DATA VOLUME

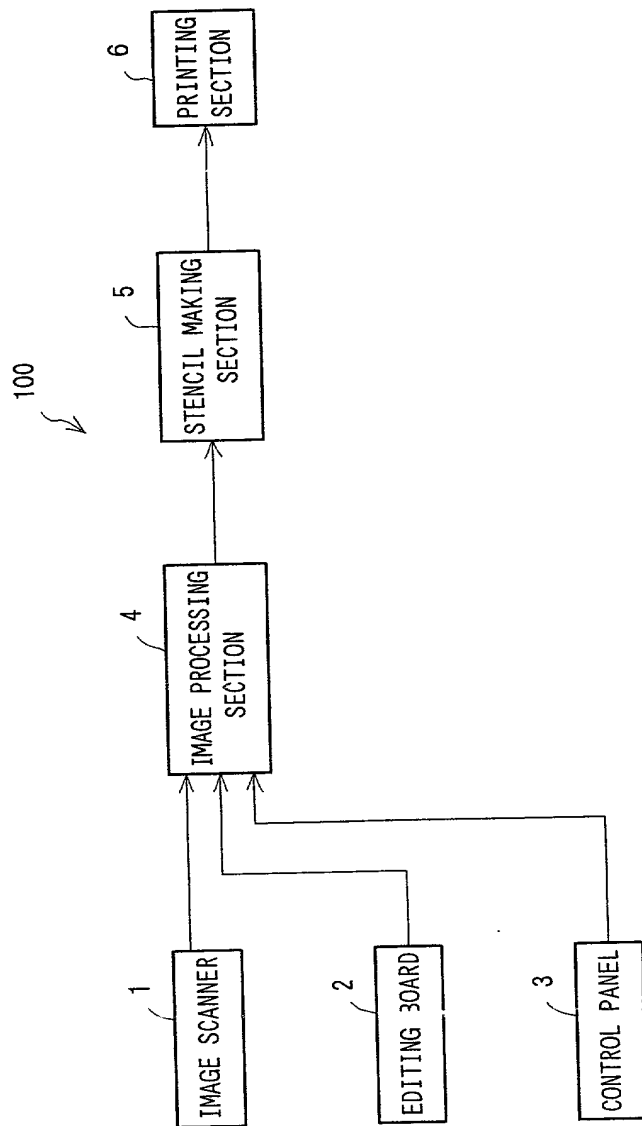


FIG. 6

FIG. 7A

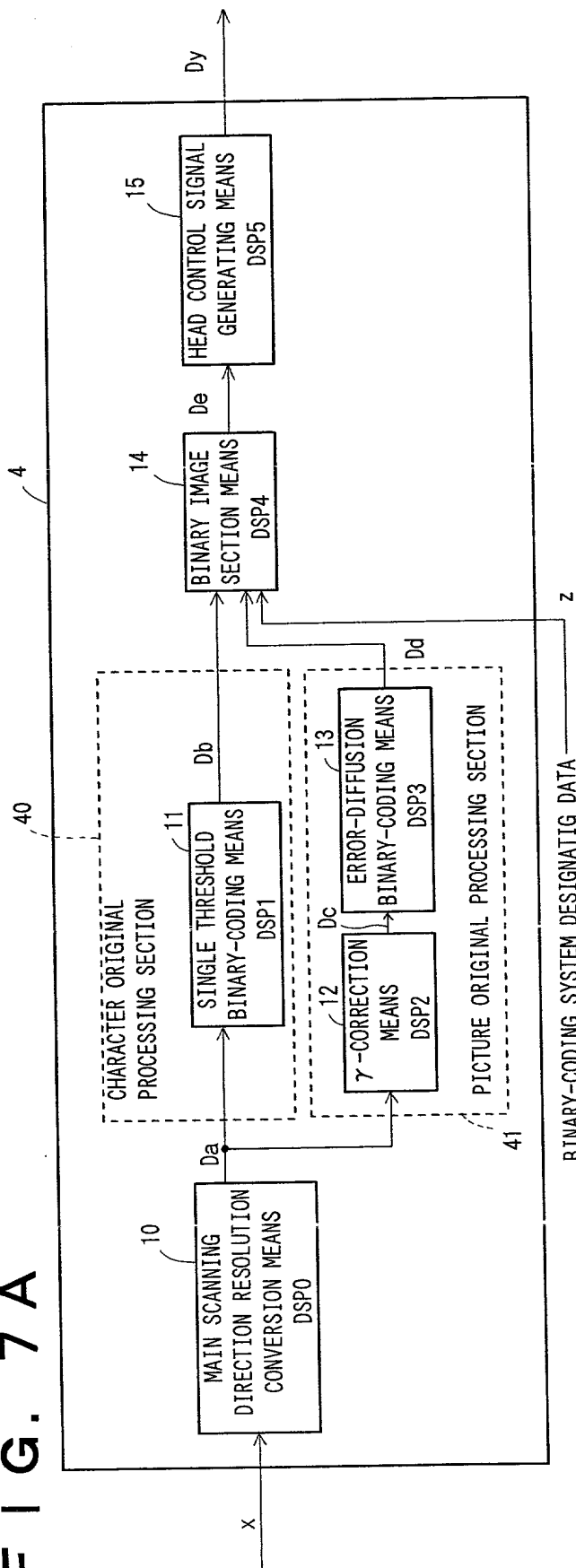
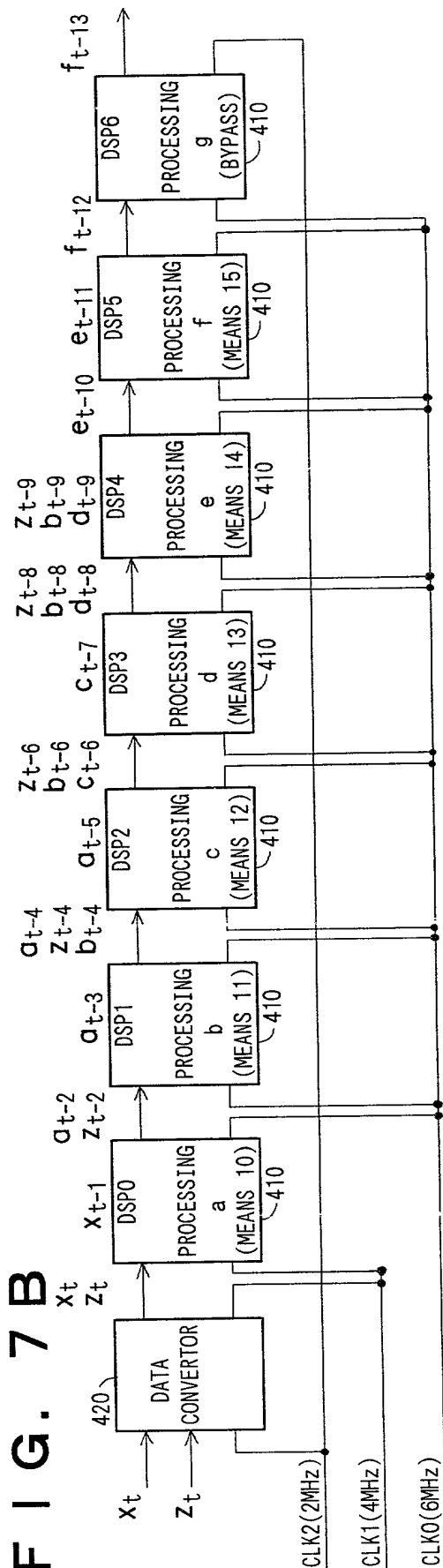
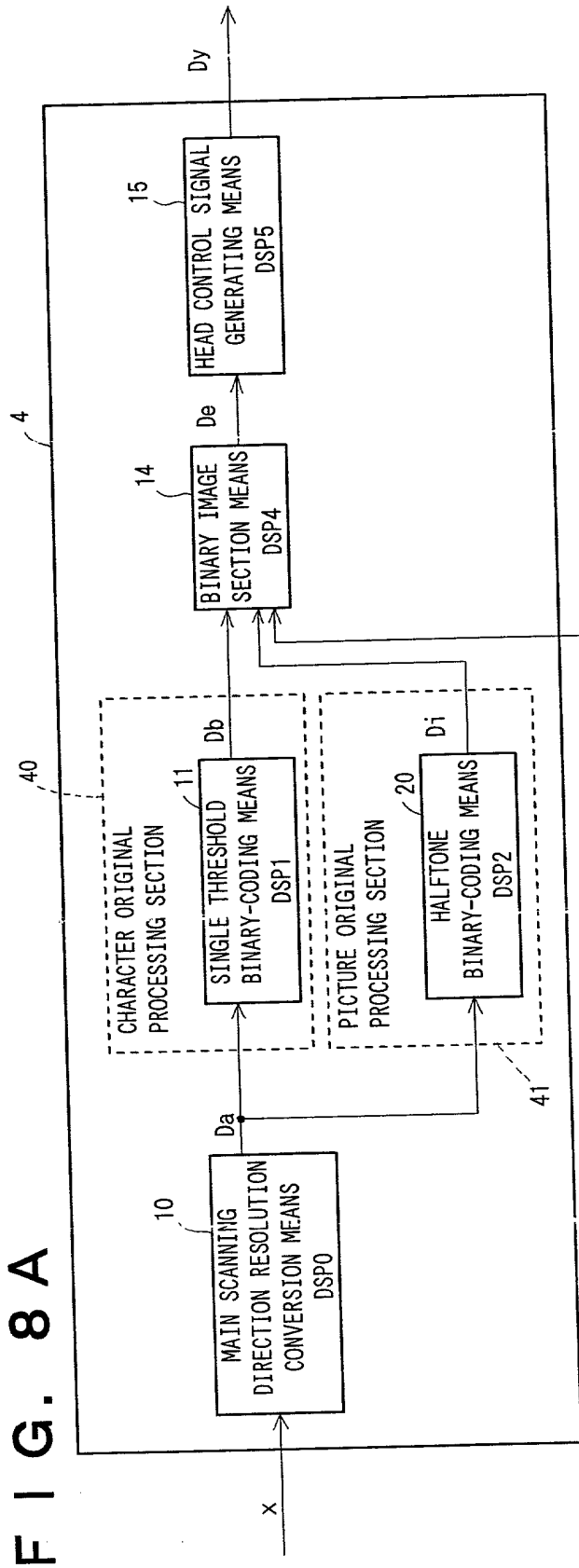


FIG. 7B





BINARY-CODING SYSTEM DESIGNATING DATA

FIG. 8B

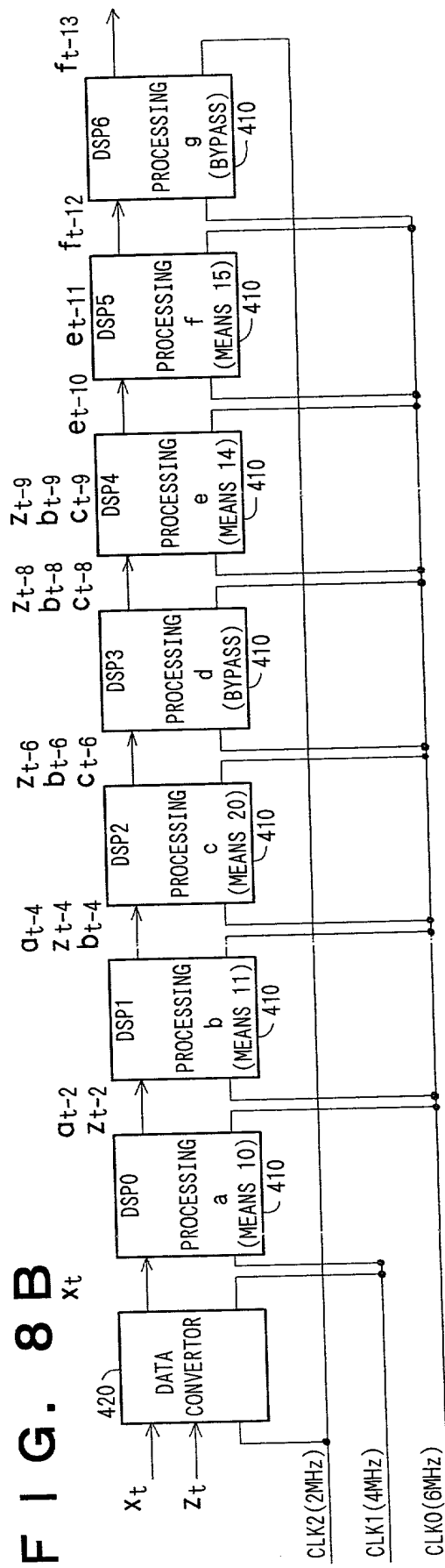


FIG. 9A

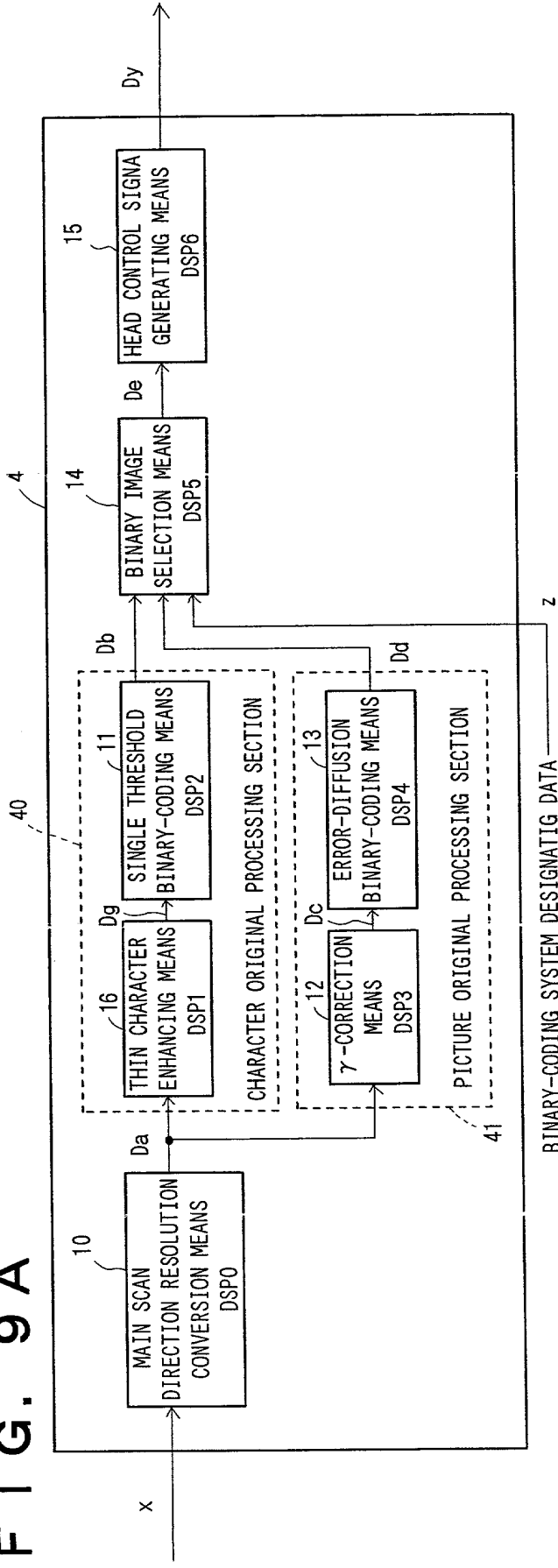


FIG. 9B

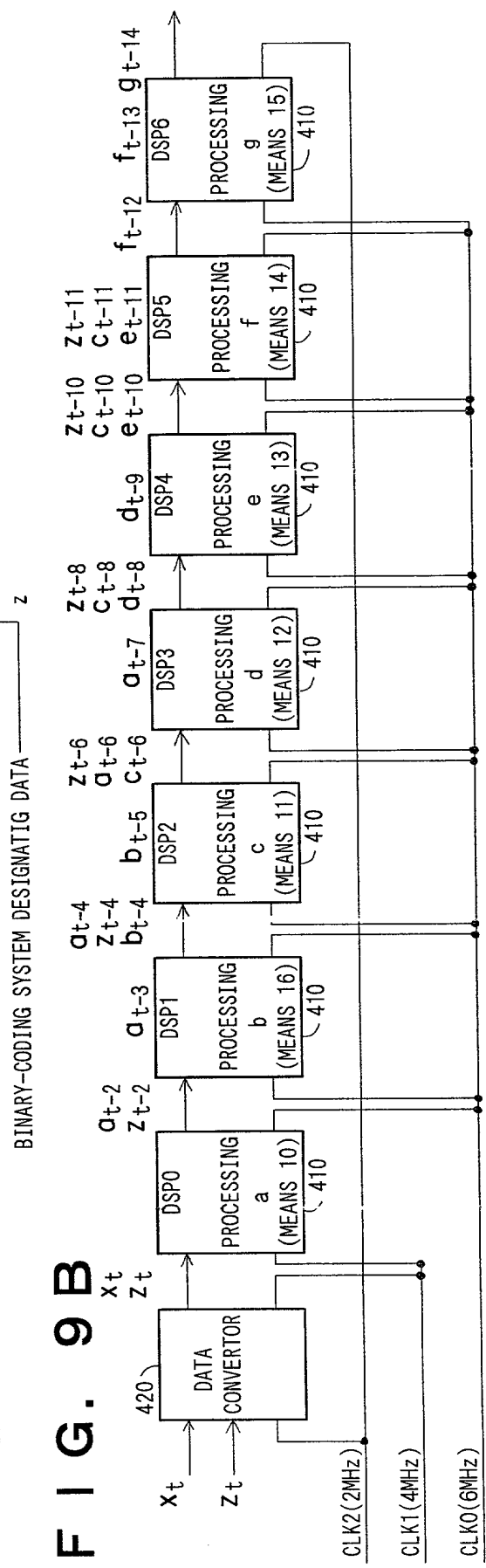




FIG. 10A

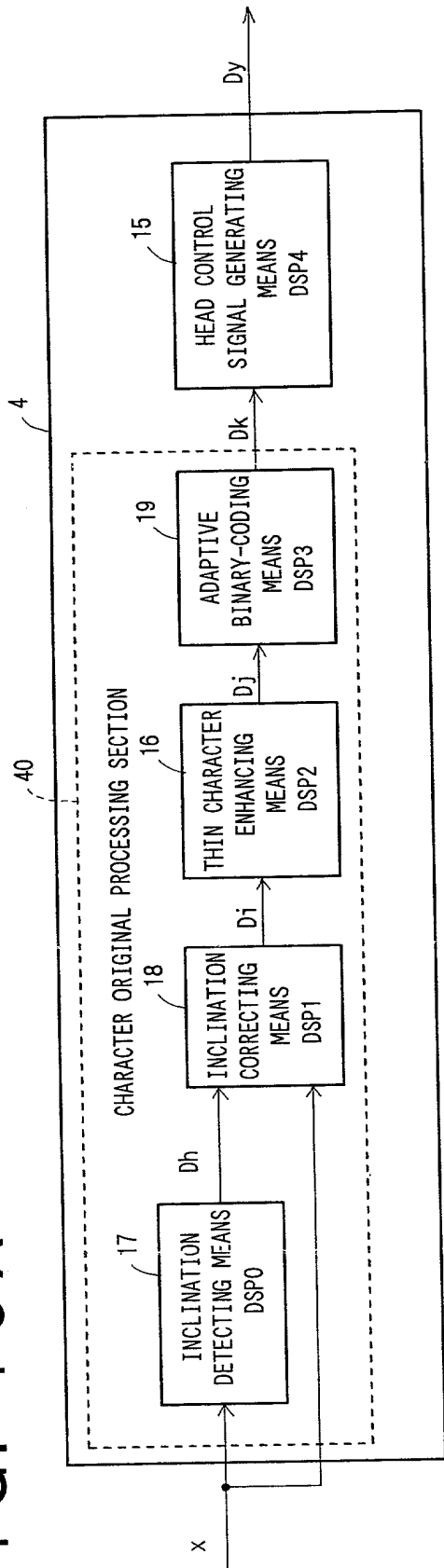
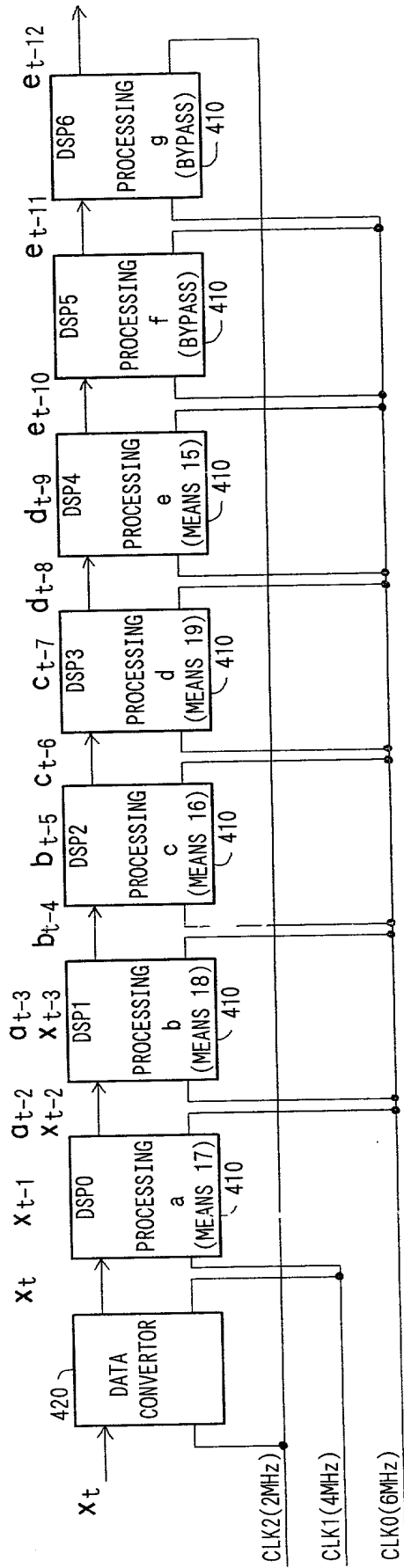


FIG. 10B



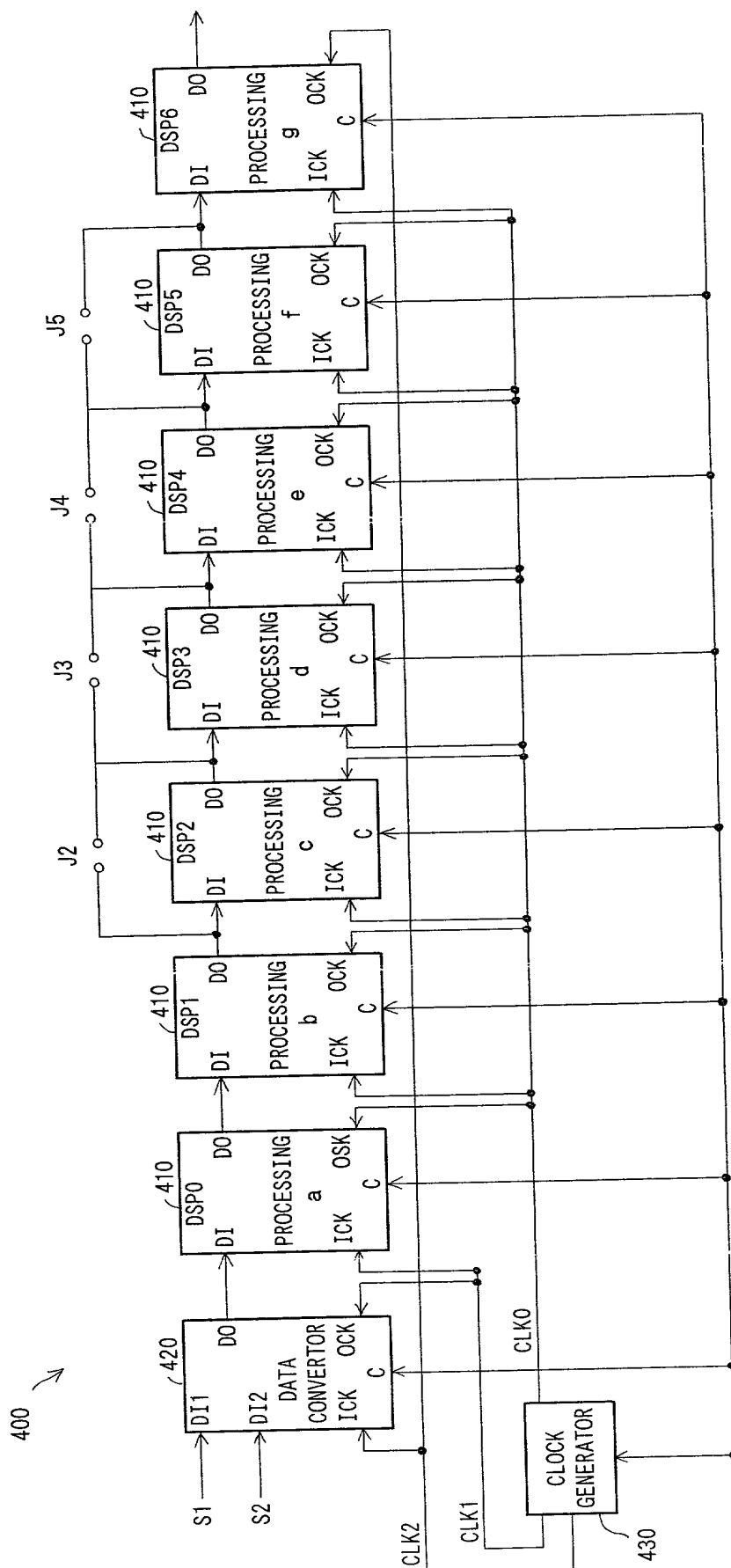


FIG. 11

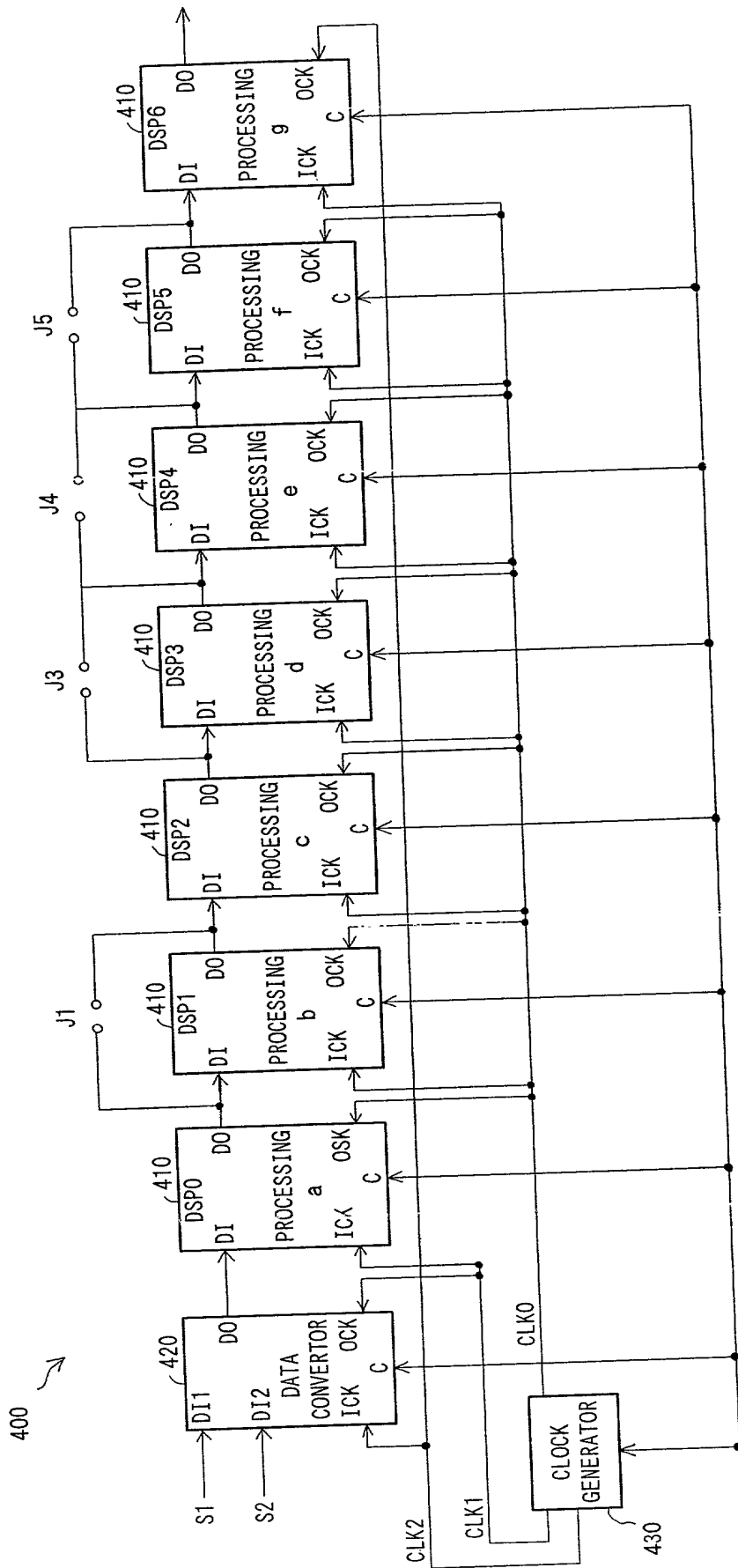


FIG. 12

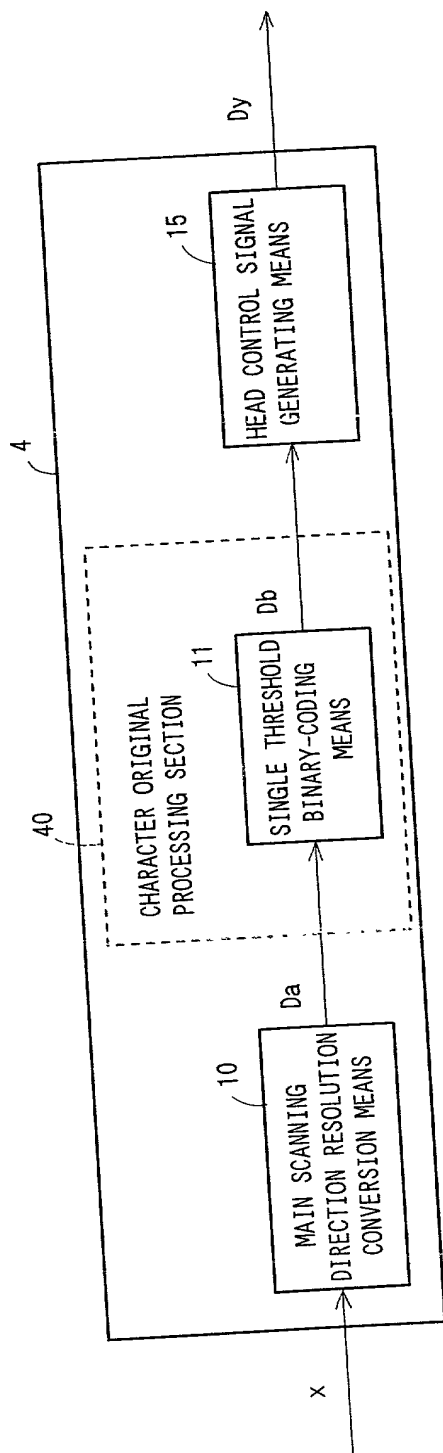


FIG. 13

FIG. 14A

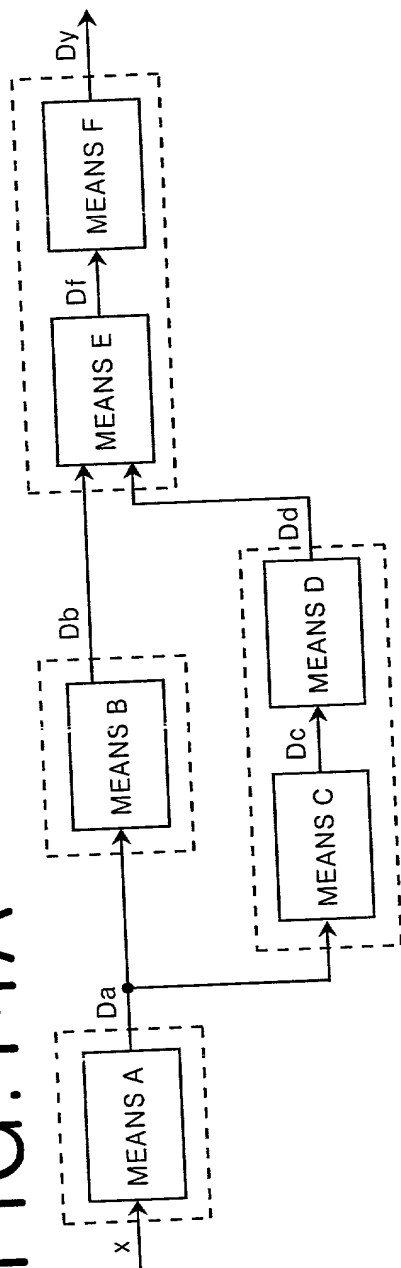


FIG. 14B

